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1. Document ID: JP 3082701 B2, EP 864983 A2, JP 10260901 A, KR 98080255 A

L1: Entry 1 of 5

File: DWPI

Aug 28, 2000

DERWENT-ACC-NO: 1998-469486

DERWENT-WEEK: 200044

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TITLE: Memory protection circuit for illegal write prevention - converts address by masking memory address to switch access to predetermined validated region, for access to regions other than predetermined region validated for access

INVENTOR: KATSUTA, H

PRIORITY-DATA: 1997JP-0082330 (March 14, 1997)

PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
JP 3082701 B2	August 28, 2000		006	G06F012/14
EP 864983 A2	September 16, 1998	E	009	G06F012/14
JP 10260901 A	September 29, 1998		006	G06F012/14
KR 98080255 A	November 25, 1998		000	G06F012/16

INT-CL (IPC): G06 F 12/14; G06 F 12/16

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMPC
Draw Desc	Clip Img	Image									

2. Document ID: TW 363153 A, EP 811921 A2, JP 10083343 A, US 5813041 A, KR 98004047 A, US 5875482 A, CN 1206877 A, US 6006288 A

L1: Entry 2 of 5

File: DWPI

Jul 1, 1999

DERWENT-ACC-NO: 1998-021126

DERWENT-WEEK: 200029

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TITLE: Memory accessing method in data processing system - involves speculatively activating first chip select signal if memory access is predicted to be to predetermined region

INVENTOR: COLLINS, C M; LINDQUIST, S P ; MCINTYRE, K L ; PECHONIS, D W ; REIPOLD, A M ; WINTER, R L

PRIORITY-DATA: 1996US-0660702 (June 6, 1996), 1996US-0660028 (June 6, 1996), 1996US-0660620 (June 6, 1996), 1997CN-0115454 (July 24, 1997)

PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
TW 363153 A	July 1, 1999		000	G06F012/00
EP 811921 A2	December 10, 1997	E	017	G06F012/02
JP 10083343 A	March 31, 1998		015	G06F012/02
US 5813041 A	September 22, 1998		000	G06F012/00
KR 98004047 A	March 30, 1998		000	G06F011/26
US 5875482 A	February 23, 1999		000	G06F012/00
CN 1206877 A	February 3, 1999		000	G06F012/00
US 6006288 A	December 21, 1999		000	G06F009/26

INT-CL (IPC): G06 F 9/26; G06 F 11/26; G06 F 12/00; G06 F 12/02; G11 C 8/00

3. Document ID: JP 07262376 A

L1: Entry 3 of 5 File: DWPI Oct. 13, 1995

DERWENT-ACC-NO: 1995-386741

DERWENT-WEEK: 199550

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TITLE: Colour-image processor for distinguishing specific image
e.g. forged or valuable document in e.g. colour copying machine or
printer - compares each colour component of input colour-image data
with colour component of specific image stored in memory to
determine predetermined region of image data NoAbstract

PRIORITY- DATA: 1994JP-0055527 (March 25, 1994)

PATENT - FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
JP 07262376 A	October 13, 1995		011	G06T007/00

INT-CL (IPC): G03 G 15/01; G03 G 21/04; G06 T 7/00; G07 D 7/00; H04 N 1/40

4. Document ID: US 5363358 A

L1: Entry 4 of 5

~~File: DWPI~~

Nov 8, 1994

DERWENT-ACC-NO: 1994-357652

DERWENT-WEEK: 199444

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TITLE: Optical disc recording appts. with selection of three-beam and push=pull systems - compares signal level read out from region other than predetermined region of disc by pick-up and level setting signal stored in memory

INVENTOR: YANAGAWA, N

PRIORITY-DATA: 1992JP-0313619 (November 24, 1992)

PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
US 5363358 A	November 8, 1994		011	G11B007/095

INT-CL (IPC): G11B 7/095

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	KMC
Drawn Desc	Clip Img	Image								

5. Document ID: JP 61273068 A

L1: Entry 5 of 5 File: DWPI Dec 3, 1986

DERWENT-ACC-NO: 1987-016889

DERWENT-WEEK: 198703

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TITLE: Video signal transmitter - has memory and circuit to engage predetermined region as memory region of video information

NoAbstract Dwg 1/3

PRIORITY-DATA: 1985JP-0113978 (May 29, 1985)

PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
JP 61273068 A	December 3, 1986		007	

INT-CL (IPC): H04N 1/21; H04N 7/00

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	KMC
Drawn Desc	Image									

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Term	Documents
MEMORY.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	300139
MEMORIES.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	11934
MEMORYS.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	1
PREDETERMINED.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	130162
PREDETERMINEDS.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	1
REGION.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	61928
REGIONS.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	27335
((PREDETERMINED ADJ REGION) AND MEMORY).TI..USPT,PGPB,JPAB,EPAB,DWPI,TDBD.	5
((MEMORY AND PREDETERMINED REGION).TI.).USPT,PGPB,JPAB,EPAB,DWPI,TDBD.	5

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MEMORYS.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	153
LOCAT\$	0
LOCAT.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	122
LOCATA.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	2
LOCATAB.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	1
LOCATABE.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	1
LOCatability.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	63
LOCATABL.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	1
LOCATABLE.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	8013
LOCAT\$(LOCATION/ANGLES).USPT,PGPB,JPAB,EPAB,DWPI,TDBD.	pickup term
(L7 AND (MEMORY NEAR5 LOCAT\$)).USPT,PGPB,JPAB,EPAB,DWPI,TDBD.	0

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Search History

DATE: Monday, April 29, 2002 [Printable Copy](#) [Create Case](#)

<u>Set Name</u> side by side	<u>Query</u>	<u>Hit Count</u>	<u>Set Name</u> result set
<i>DB=USPT,PGPB,JPAB,EPAB,DWPI,TDBD; PLUR=YES; OP=ADJ</i>			
<u>L10</u>	L7 and (memory near5 locat\$)	0	<u>L10</u>
<u>L9</u>	L7 and (prohib\$ near5 region\$1)	0	<u>L9</u>
<u>L8</u>	L7 and predeterm\$	0	<u>L8</u>
<u>L7</u>	L6 and memory	1	<u>L7</u>
<u>L6</u>	5893139.pn.	2	<u>L6</u>
<u>L5</u>	L2 and ((711/\$)!.CCLS.)	1	<u>L5</u>
<u>L4</u>	L3 and ((711/\$)!.CCLS.)	5	<u>L4</u>
<u>L3</u>	((memory) near5 (predetermin\$ region\$1)).clm.	58	<u>L3</u>
<u>L2</u>	((memory) near5 (predetermin\$ region\$1)).ab.	56	<u>L2</u>
<u>L1</u>	(memory and predetermined region).ti.	5	<u>L1</u>

END OF SEARCH HISTORY